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Johnie Crasby

(Signature of person mailing paper or fee)

68340 U.S. PTO  
08853323  
05/08/97

**REQUEST FOR FILING A PATENT APPLICATION UNDER 37 CFR 1.60**

Case No.	ANTICIPATED CLASSIFICATION OF THIS APPLICATION		PRIOR APPLICATION EXAMINER	ART UNIT
	CLASS	SUBCLASS		
7103/30			G. Nguyen	3203

Address to:

Assistant Commissioner for Patents  
Washington, DC 20231

This is a request for filing a ☐ continuation ☒ divisional application under 37 CFR 1.60, of pending prior application Number 08/759,172, filed on December 3, 1996, entitled LINEAR POLISHER AND METHOD FOR SEMICONDUCTOR WAFER PLANARIZATION.

1. Enclosed is a copy of the latest inventor-signed prior application, including a copy of the oath or declaration showing the original signature or an indication it was signed. I hereby verify that the papers are a true copy of the latest signed prior application number 08/759,172, and further that all statements made herein of my own knowledge are true; and further that these statements were made with the knowledge that willful, false statements and the like are made punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful statements may jeopardize the validity of the application or any patent issuing thereon.

CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
	TOTAL CLAIMS (37 CFR 1.16(c))	- 20 =		x \$ 22 =	\$
	INDEPENDENT CLAIMS (37 CFR 1.16(b))	4 - 3 =		x \$ 80 =	\$ 80.00
	MULTIPLE DEPENDENT CLAIMS (if applicable) (37 CFR 1.16(d))			+ \$260 =	\$ 0.00
				BASIC FEE (37 CFR 1.16(a))	\$ 770
				Total of above Calculations =	\$
	Reduction by 50% for filing by small entity (Note 37 CFR 1.9, 1.27, 1.28)				\$
				TOTAL =	\$ 850.00

2. ☐ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27

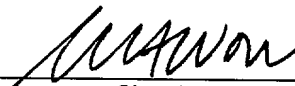
☐ is enclosed.

☐ was filed in prior application number \_\_\_\_\_ and such status is still proper and desired (37 CFR 1.28(a)).

3. ☒ The Assistant Commissioner is hereby authorized to charge any fees which may be required under 37 CFR 1.16 and 1.17, or credit any overpayment to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.
4. ☒ A check in the amount of \$ 850.00 is enclosed.
5. ☒ Cancel in this application original claims 1-22 of the prior application before calculating the filing fee. (At least one original independent claim must be retained for filing purposes).
6. ☒ The inventor(s) of the invention being claimed in this application is(are): Homayoun TALIEH and David E. WELDON.
7. ☐ This application is being filed by less than all the inventors named in the prior application. In accordance with 37 CFR 1.60(b). The Assistant Commissioner is requested to delete the name(s) of the following person or persons who are not inventors of the invention being claimed in this application: \_\_\_\_\_.
8. ☒ Amend the specification by inserting before the first line the sentence: "This application is a ☐ continuation ☒ division of application number 08/759,172, filed December 3, 1996, (pending), which is in turn a continuation of application Serial No. 08/287,658 filed August 9, 1994 (abandoned)."
9. ☒ New formal drawings are enclosed.
10. ☐ Priority of foreign application number \_\_\_\_\_, filed on \_\_\_\_\_ in \_\_\_\_\_ is claimed under 35 U.S.C. 119.  
☐ The certified copy has been filed in prior application number \_\_\_\_\_, filed \_\_\_\_\_.
11. ☒ A preliminary amendment is enclosed.
12. ☒ The prior application is assigned of record to OnTrak Systems, Inc..
13. ☐ Also enclosed: \_\_\_\_\_.
14. ☒ The power of attorney in the prior application is to: William A. Webb and Michael P. Chu.  
a. ☐ The power appears in the original papers in the prior application.  
b. ☒ Since the power does not appear in the original papers, a copy of the power in the prior application is enclosed.  
c. ☐ Address all future correspondence to: (may one be completed by applicant, or attorney or agent of record.)

5-8-97  
Date

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Signature  
Name: William A. Webb  
Reg. No. 28,277

- ☐ Inventor(s)  
☐ Assignee of complete interest  
☒ Attorney or agent of record  
☐ Filed under 37 CFR 1.34(a)  
Registration Number if acting under 37 CFR 1.34(a): \_\_\_\_\_.

- 2 -

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JOHNIE CROSBY  
(Typed or printed name of person mailing paper or fee)

Johnie Crosby  
(Signature of person mailing paper or fee)

Our Case No. 7103/30

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
)  
Homayoun Talieh and David E. Weldon )  
)  
Serial No. To Be Assigned ) Examiner  
)  
Filing Date: Herewith ) Group Art Unit No.  
)  
For Linear Polisher and Method for )  
Semiconductor Wafer Planarization )

**PRELIMINARY AMENDMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Please enter the following amendments prior to examination:

**In the Claims**

Please enter new Claims 32-35 as follows:

- 32. A polishing pad assembly for polishing a semiconductor wafer comprising:  
a belt forming a closed loop; and  
at least one polishing pad mounted on the belt.--
- 33. The polishing pad assembly of Claim 32 in which said belt is formed of metal.—
- 34. A polishing pad assembly for polishing a semiconductor wafer comprising:  
a first roller;  
at least one additional roller;  
a belt forming a closed loop, which belt is mounted on said first roller and  
said at least one additional roller;  
at least one polishing pad mounted to said belt; and  
a drive system coupled to at least said first roller to rotate said first roller  
and to cause said belt and said polishing pad to move in a path.--
- 35. The polishing pad assembly of Claim 34 in which said belt is formed of metal.--

### REMARKS

New Claims 32-35 correspond to Claims 32-35 as added by amendment in Application Serial No. 08/2987,658 prior to the restriction requirement in that application.

Respectfully submitted,



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William A. Webb  
Registration No. 28,277  
Attorney for Applicant

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# LINEAR POLISHER AND METHOD FOR SEMICONDUCTOR WAFER PLANARIZATION

## Background of the Invention

5 This invention relates in general to a system for chemical mechanical polishing of semiconductor wafers. More particularly, the present invention relates to a linear polisher for the chemical mechanical planarization of semiconductor wafers.

10 The available systems for the chemical mechanical planarization of semiconductor wafers typically employ a rotating wafer holder for supporting the wafer and a polishing pad which is rotated relative to the wafer surface. The wafer holder presses the wafer surface against the polishing pad during the planarization process and rotates the wafer about a first axis relative to the polishing pad. The polishing pad is carried by a polishing wheel or platen which is rotated about a second axis different from the rotational axis of the wafer holder. A polishing agent or slurry is applied to the polishing pad to polish the wafer. As the wafer holder and the polishing wheel are each rotated about their  
15 respective central axes, an arm moves the wafer holder in a direction parallel to the surface of the polishing wheel.

Since the polishing rate applied to the wafer surface is proportional to the relative velocity of the polishing pad, the polishing rate at a selected point on the wafer surface depends upon the distance of the selected point from the axis of rotation. Thus, the  
20 polishing rate applied to the edge of the wafer closest to the rotational axis of the polishing pad is less than the polishing rate applied to the opposite edge of the wafer. Rotating the wafer throughout the planarization process averages the polishing rate applied across the wafer surface so that a uniform average polishing rate is applied to the wafer surface. Although the average polishing rate may be uniform, the wafer surface is  
25 continuously exposed to a variable polishing rate during the planarization process.

Although the polishing rate is generally proportional to the relative velocity of the polishing pad, other factors as for example fluid dynamic and thermodynamic effects

on the chemical reactions occurring during the planarization process influence the actual polishing rate at any given instant in time. These effects are not uniform across the wafer surface during the planarization process. Moreover, instead of "averaging" the effects, the relative rotation of the wafer and the polishing pad contribute to the fluid dynamics and thermodynamics of the reaction.

After a period of time, the polishing pad becomes saturated with deactivated slurry, loose particles, etc. The pad must be frequently roughened to remove such particles from the polishing surface of the pad. For example, a scraping tool is typically mounted in contact with the polishing pad to scrape the loose slurry from the pad surface.

Because of advances in wafer processing technology and semiconductor component structure, uniformly polishing or planarizing a film on the surface of the wafer has become increasingly important. For example, integrated circuits such as microprocessors, controllers and other high performance electronic logic devices have become increasingly complex while the size of such devices has decreased substantially. With the multiple wiring layers employed in complex devices, a significant component of the delay in signal propagation is due to the interconnections between the multiple layers. Several multilevel interconnection processes are being developed to reduce the delays associated with interconnect resistance, such as smaller wiring geometry and the use of copper or other materials as interconnect metals. However, the surface of the semiconductor wafer is generally rough. Each wiring layer provides additional circuitry components which project from the wafer surface, producing a rippled effect on the surface of the device. When several layers are formed on the wafer, the uneven topography of the device becomes more exaggerated. Even if the first layer is completely planar, circuitry components of the succeeding layers often produce a rippled effect which must be planarized.

This invention provides a system for uniformly polishing the surface of a semiconductor wafer. The system includes a linear polisher which applies a uniform polishing rate across the wafer surface throughout the planarization process for uniformly polishing the film on the surface of the semiconductor wafer. The polisher is of simplified construction, thereby reducing the size of the machine and making the polisher suitable for even larger-diameter wafers. For example, the linear polisher is approximately 1/5

the size of available machines. The reduced size and simplicity of the machine substantially reduces the manufacturing costs of the polisher. Since less space is required for the polisher, the operation costs are also substantially reduced. Although the overall size may vary, the linear polisher may be only slightly larger than the wafer. The polisher  
5 of the invention may have one or more conditioning stations for roughing or conditioning the polishing member during the polishing cycle, ensuring that a uniform polishing rate is applied to the wafer surface throughout the planarization process.

#### Summary of the Invention

In summary, the present invention provides a system for the chemical mechanical  
10 planarization of semiconductor wafers. The system includes a wafer polishing machine having a linear polisher and a wafer support assembly for holding a semiconductor wafer. The linear polisher includes a polishing pad positioned to engage the wafer surface. The polishing pad is moved in a linear direction relative to the wafer for uniformly planarizing the surface of the wafer. The wafer polishing machine may also include a pivotal  
15 alignment device positioned to pivotally support either the wafer holder or the polishing pad so that the wafer surface and the polishing pad are retained in parallel alignment during operation of the polishing machine.

In one embodiment of the invention, the polishing pad is movable in a continuous path during which the polishing pad passes across the surface of the wafer. The wafer  
20 polishing machine further includes a conditioning station positioned in the path of the polishing pad for conditioning the pad during operation of the polishing machine.

The system of the invention also includes a method for uniformly polishing the surface of a semiconductor wafer. The method includes the steps of supporting the wafer with the surface of the wafer engaging the polishing pad and moving the polishing pad  
25 in a linear direction relative to the wafer to apply a uniform polishing force across the wafer surface.

#### Brief Description of the Drawings

Additional objects and features of the invention will be more readily apparent from the following detailed description and appended claims when taken in conjunction  
30 with the drawings, wherein:

Figure 1 is front plan view of a wafer polishing machine in accordance with the invention;

Figure 2 is a side plan view, partially broken away, of the wafer polishing machine of Figure 1;

5        Figure 3 is a top plan view of the wafer polishing machine of Figure 1;

Figures 4A and 4B are schematic side views showing the support assembly is a raised position and a lowered position;

Figures 5A and 5B are schematic views of a wafer polishing machine in accordance with another embodiment of the invention;

10       Figure 6 is a perspective view of a linear polisher of a wafer polishing machine in accordance with another embodiment of the invention;

Figure 7 is a schematic view of the wafer polishing machine of Figure 6;

Figure 8 is a perspective view of a linear polisher in accordance with still another embodiment of the invention; and

15       Figure 9 is a view similar to Figure 8 of a linear polisher in accordance with another embodiment of the invention.

#### Description of the Preferred Embodiments

Reference will now be made in detail to the preferred embodiment, which is illustrated in the accompanying figures. Turning now to the drawings, wherein like  
20       components are designated by like reference numerals throughout the various figures, attention is directed to Figures 1-3.

A wafer polishing machine 10 for uniformly planarizing the surfaces of a semiconductor wafer 8 is shown in Figures 1-3. The polishing machine 10 generally includes a linear polisher 12 having a polishing member or polishing pad 14 for polishing the  
25       surface 9 of the semiconductor wafer 8 and a support assembly 16 for supporting the semiconductor wafer during the polishing operation. A polishing agent or slurry (not shown) such as a colloidal silica or fumed silica slurry is deposited on the polishing member to polish the wafer surface. Alternatively, the polishing member 14 may be provided by a pad impregnated with an abrasive polishing agent. The linear polisher  
30       12 moves the polishing pad 14 in a linear direction relative to the semiconductor wafer 8 to continuously provide a uniform polishing force across the entire surface of the wafer.



Preferably, the polishing member 14 is moved at a constant velocity although in some applications it may be desirable to employ a specific variable velocity profile to polish the wafer surface. The linear, constant velocity motion of the polishing member 14 provides superior polishing uniformity across the wafer surface.

5 In the embodiment of the linear polisher 12 shown in Figures 1-3, the polishing member or pad 14 is mounted to the outer surface of an endless belt 18. The belt 18 extends across a support plate 20 and is mounted to a pair of rollers 22 and 24. A motor assembly 26 coupled to the rollers 22 and 24 drives the rollers so that the belt 18 is moved at a constant velocity in the direction indicated by arrow A. As the belt is moved by  
10 the rollers, the belt 18 travels across the support surface 20. The support surface 20 rigidly supports the belt 18 opposite the support assembly 16 to ensure that the polishing member 14 applies a uniform polishing force across the entire surface of the wafer. Preferably, the velocity at which the belt is moved is within the range of approximately 50 to 150 feet per minute for optimum planarization of the wafer surface. However, it is to be  
15 understood that depending upon the chemistry employed, the velocity may also be considerably faster, for example up to 300 feet per minute or more. A fluid layer, generally designated 28, between the inner surface of the belt 18 and the support plate 20 reduces frictional losses and minimizes heat dissipation during operation of the linear polisher 10. The fluid layer 28 may also permit minimal deflection of the belt 18 relative  
20 to the support plate as it passes across the plate 20 to facilitate the parallel alignment of the wafer surface and the polishing member 14.

The polishing member 14 preferably extends the entire circumference of the endless belt 18 and has a width greater than the diameter of the wafer 8. However, the size of the polishing member may be varied as desired. The polishing pad 14 is affixed to the  
25 belt 18 using any suitable securement means. If the polishing member is originally rectangular in shape, the overlapping edges of the polishing member 14 are tapered so that the wafer 8 tends to press the uppermost edge of the polishing member against the underlying edge. In the present embodiment, the polishing member 14 is a pad of stiff polyurethane material, although other suitable materials may also be used. The endless  
30 belt may be formed of a metal such as stainless steel, high strength polymers such as polyethylene terephthalate resin, or other suitable flexible materials having sufficient

strength to withstand the loads applied to the belt by the wafer 8. In the embodiment shown in Figures 1-3, the endless belt 18 is carried by two rollers 22 and 24. However, it is to be understood that the number of rollers may be increased as desired. The rollers 22 and 24 retain the belt 18 under tension so that the polishing member 14 is sufficiently  
5 rigid to uniformly polish the surface of the wafer. The tension of the belt may be increased or decreased as necessary by adjusting the position of roller 24 relative to roller 22.

The support assembly 16 retains the wafer 8 in position during the polishing operation. In the embodiment shown in Figures 1-3, the support assembly 16 also maximizes the parallel alignment between the wafer surface 9 and the polishing member  
10 14 and applies a downward force pushing the wafer surface 9 against the polishing member 14 so that the polishing member 14 applies the required polishing force across the surface of the wafer. As shown particularly in Figure 2, the support assembly 16 includes a wafer holder 34 for supporting the wafer 8 and accurately aligning the wafer surface 9 with the polishing member 14. The wafer holder 34 has a lower plate 36 formed with a disc-  
15 shaped recess shaped to receive the wafer 8 with the wafer surface 9 projecting slightly from the lower plate 36. The wafer 8 is held in place by a backing film, waxing or another suitable technique. The lower plate 36 is affixed to a spherical-shaped journal 40 supported in a bearing 42. In the present embodiment, the clearance spacing between the journal 40 and the bearing 42 is filled with a lubricant such as water, another slurry  
20 compatible liquid or a suitable gas. The lubricant-filled cavity is coupled to a reservoir (not shown) in which a supply of lubricant is retained under pressure to provide a hydrostatic bearing in which the journal 40 is completely isolated from the bearing 42 at all times.

The spherical curvature of the journal 40 and bearing 42 provides a pivotal support  
25 for the wafer 8 which retains the wafer surface 9 at an orientation parallel to the surface of the polishing member 14 regardless of the shear forces applied to the wafer surface during the polishing process. In the present embodiment, the journal 40 is shaped in the form of a slab or section of a sphere having a center located at pivot point 46 located on the surface 9 of the wafer as shown in Figures 1 and 2. In other words, the shape  
30 of the journal 40 may be obtained by sectioning the sphere into two hemispheres and then removing a slice having the same thickness as the wafer from the planar surface

of one of the hemispheres. This ensures that the pivot point 46 is located on the surface of the wafer. As shown in Figures 1 and 2, a section may optionally be removed from the opposite end of the hemisphere to reduce the height of the journal 40.

5 The journal 40 pivots within the bearing 42 to provide the wafer surface 9 and the polishing pad 14 with a substantially parallel orientation throughout the polishing operation. The journal 40 pivots about the pivot point 46 so that the surface of wafer having a tapered thickness is parallel to the polishing member 14. The journal also accommodates variations in the thickness of the belt 18 and polishing member 14 so that the parallelism between the wafer surface 9 and the polishing member 14 is maintained.

10 When the wafer surface is positioned against the moving polishing belt 14, shear frictional forces are applied across the wafer surface. Since the frictional forces applied to the wafer essentially pass through the pivot point 46, the frictional forces will not cause the journal 40 to pivot relative to the bearing 42. Instead, the journal 40 continues to position the wafer with the wafer surface 9 parallel to the polishing member 14. Thus, by positioning

15 the pivot point of the journal 40 on the wafer surface 9, the wafer holder 34 of the invention maintains the parallelism between the wafer surface 9 and the polishing member 14 so that the entire wafer surface may be uniformly polished.

As the wafer is polished and the thickness of the wafer is reduced, the pivot point 46 become displaced from the surface of the wafer. Often, the change in wafer thickness

20 is so small that the parallel alignment of the wafer surface and the polishing member 14 will not be significantly affected. However, if greater precision is required, journal 40 may be formed with a wedge shaped section (not shown). As the wafer thickness is reduced, the wedge shaped section slides relative to the remainder of the journal to maintain the wafer surface at the center of the sphere or pivot point 46. Depending upon

25 the vibrational effect of the polishing machine 10, it may also be desirable to include a closed-loop control system (not shown) to provide damping since the journal 40 and bearing 42 are substantially frictionless.

The wafer holder 34 is mounted to a horizontally extending upper platform 48 positioned above the support plate 20 of the linear polisher 12. The upper platform 48

30 is carried by a vertically extending back plate 50. The back plate 50 is pivotally mounted to the linear polishing assembly 12 by a transversely extending pivot bar 52. The support

assembly 16 may be easily moved away from the polishing member 14, endless belt 18 and support plate 20 for insertion and removal of the wafer or maintenance of the support assembly or linear polisher by pivoting the assembly 16 about the bar 52.

5 The upper platform 48 of the support assembly 16 is coupled to the linear polisher by a pneumatic cylinder 54. When the pneumatic cylinder is actuated, the cylinder 54 urges the platform 48 toward the support plate 20 to press the wafer 8 against the polishing member 14 of the linear polisher. Figures 4A and 4B schematically show the support assembly 16 in a raised position and a lowered position, respectively. By moving the upper platform 48 downward, the required polishing force is applied to the surface  
10 of the wafer for planarizing the wafer surface. The magnitude of the polishing force applied to the wafer surface 9 may be precisely controlled by controlling the operation of the pneumatic cylinder 54. In other embodiments of the invention, a hydraulic cylinder or other device may be used instead of the pneumatic cylinder 54 to move the upper platform 48 toward the support plate 20.

15 Preferably, the support assembly 16 slowly rotates the wafer 8 relative to the polishing member as the polishing member 14 is moved in linear direction. When the polishing member 14 engages the wafer 8, polishing pathways are formed on a microstructural level. Slow rotation of the wafer allows for polishing to occur at random incidence (i.e. in random directions), an important factor in defining geometric structures with  
20 polishing and preventing the formation of defined scratches in the polished surface. With most surface configurations, it is generally desirable to provide the pathways with random trajectories. Slowly rotating the wafer also varies the location of the leading edge to obtain uniform polishing along the edge of the wafer. In the present embodiment, the wafer holder 34 is slowly rotated relative to the polishing member 14 by a motor (not shown)  
25 at a slow rate. The rate of rotation of the wafer holder 34 is less than 1/10 of the speed of the belt 18 and is selected so that the wafer undergoes a number of full revolutions during the polishing operation to achieve uniform polishing. At a minimum, the wafer be rotated for a full rotation during the polishing process. Rotating the wafer for less than a full revolution may provide the wafer surface with a non-uniform profile.

30 The uniform polishing rate applied across the wafer surface by the linear motion of the polishing member 14 and the parallelism achieved between the wafer surface 9

and the polishing member 14 allows for uniform polishing with increased precision. This is of particular advantage in the processing of semiconductor wafers, where one may wish to remove one micron from a film having a thickness of two microns.

5 A wafer polishing machine 10a in accordance with another embodiment of the invention is shown schematically in Figures 5A and 5B. Referring particularly to Figure 5A, the polishing machine 10a generally includes a linear polisher 12a having a polishing member 14a mounted to an endless belt 18a which is carried by a plurality of rollers 65. The semiconductor wafer is retained by a support assembly 16a with the surface of the wafer positioned to engage the polishing member 14a. The belt 18a moves the  
10 polishing member 14a in a linear direction relative to the wafer to uniformly polish the surface of the wafer.

As the polishing member 14a polishes the wafer surface 9, used slurry collects within the pores in the polishing material and reduces the roughness of the polishing member 14a. The polishing member must be periodically conditioned to remove the  
15 deactivated slurry and roughen the polishing member 14, thereby maximizing the effectiveness of the polishing member 14a in uniformly planarizing the wafer surface. In the embodiment shown in Figures 5A and 5B, the linear polisher 12a includes a conditioning station 66 for conditioning the polishing member 14a during the polishing cycle. After a given section of the polishing member 14a passes across the wafer surface, it travels  
20 through the station 66 where it is conditioned before returning to the wafer surface 9. With the conditioning station 66, the wafer surface is continuously exposed to a freshly conditioned section of the polishing member 14a. Using a continuously conditioned pad to polish the semiconductor wafer provides greater control over the planarization process and ensures that the wafer surface is continuously exposed to a uniform polishing  
25 force.

In the embodiment shown in Figure 5A, the conditioning station 66 includes a scraping member 70 such as a diamond conditioning block positioned to engage the surface of the polishing member 14a after it leaves the wafer. The scraping member 70 removes loose slurry and other loose particles from the member 14a and roughens the surface  
30 of the polishing member. The polishing member 14a then passes through an acid bath 72, a rinse bath 74 and a slurry bath 76 for further conditioning. The acid bath 72

contains an acidic solution such as diluted hydrofluoric acid solution to remove the remainder of the deactivated slurry from the polishing member 14a. The rinse bath 74 is filled with a rinsing solution such as distilled water for removing any traces of the acidic solution from the polishing member. Fresh slurry, such as a colloidal silica dispersion, is applied to the polishing member 14a in the slurry bath 76. The belt 18a travels past the scraping member 70 and enters the acid bath 72. From the acid bath 72, the belt 18a passes through a first seal 78 into the rinse bath 74 and through a second seal 80 into the slurry bath 76. The seals 78 and 80 substantially prevent intermixing of the contents between the adjacent baths 72, 74 and 76. After the belt 18a leaves the slurry bath 72, the freshly conditioned polishing member 14a is passed across the wafer to polish the wafer surface.

The scraping member 70 and the series of the baths 72, 74 and 76 illustrate one configuration of a conditioning station which is particularly suitable for conditioning the polishing member 14a during operation of the wafer polishing machine 10a. However, it is to be understood that other embodiments of the invention are subject to considerable modification. For example, instead of seals 78 and 80 separating the acid bath 72, rinse bath 74 and slurry bath 76, additional rollers may be provided to direct the belt into the individual baths. The number of baths provided in the conditioning station may be increased or decreased as desired. Instead of baths, the conditioning system may employ nozzles 82 as shown in Figure 5B for spraying cleaning agents, rinsing agents and/or slurry on the polishing member 14a. Further, the conditioning system may include a combination of baths and spray injection nozzles.

Figures 6 and 7 illustrate another embodiment of a linear polisher 12b in accordance with the invention. The polishing machine 10b includes a linear polisher 12b having a polishing member 14b carried by an endless belt 18b and a support assembly 16b (Figure 7) for supporting a semiconductor wafer. As shown in Figure 7, a wafer holder 86 mounted to the support assembly 16b rigidly supports the semiconductor wafer during the polishing operation. A gimballed support 88 positioned beneath the belt 18b supports the belt 18b and applies an upward force to the belt to press the polishing member 14b against the wafer for polishing the wafer surface. The gimballed support

88 also aligns the belt 18b with the polishing member 14b parallel to the wafer surface so that a uniform polishing force is applied across the entire surface of the wafer.

In the embodiment shown in Figures 6 and 7, the construction of the gimballed support 88 is substantially similar to the wafer support 34 shown in Figures 1-3. The  
5 gimballed support 88 includes a spherical shaped journal 90 supported in a hydrostatic bearing 92. The clearance space between the journal 90 and the bearing 92 is filled with a lubricant such as water, another slurry compatible liquid or a suitable gas. A reservoir (not shown) retaining lubricant under pressure supplies the clearance space with lubricant to ensure that the journal is constantly separated from the interior of the bearing. The  
10 journal 90 has a planar support surface which engages the underside of the belt and presses the polishing member 14b against the wafer surface.

As shown in Figure 7, the journal 90 is formed in the shape of a section of a sphere which has a center at pivot point 96 positioned on the exterior of the polishing member 14b. The journal pivots within the bearing 92 about the pivot point 96 to maintain the  
15 parallelism between the wafer surface 9 and the polishing member 14b. As the polishing member 14b polishes the wafer surface, shear frictional forces are applied to the polishing member by the wafer surface. Since the frictional forces essentially pass through the pivot point 96, the frictional forces will not cause the journal 90 to pivot relative to the wafer surface. Thus, the parallelism between the surface of the wafer and the polishing  
20 member 14b is continuously maintained while the wafer surface is polished.

Instead of the endless belt of the previously described embodiments, other apparatus may be used to move the polishing member in a linear direction. Figure 8 shows a linear polisher 12c having a plurality of parallel reciprocating bars 106 positioned on a support plate 20c. A polishing member 14c is mounted to each of the reciprocating bars 106  
25 for polishing the surface of the semiconductor wafer 8. Although not shown, the bars 106 may be positioned in a slurry bath to ensure that sufficient slurry is applied to the polishing members 14c. Alternatively, the bars 106 may be inverted and suspended above the wafer and the slurry applied to the wafer surface. An actuating device such as pneumatic cylinders 108 coupled to the reciprocating bars by pins 110 move the bars  
30 in a linear direction across the support plate 20c. Although not shown, the bars 106 may be carried by linear slides or a linear motor. Preferably, the bars 106 are divided

into two groups which are simultaneously moved in opposite directions by the pneumatic cylinders 108. As shown in Figure 8, the linear polisher 12c includes four reciprocating bars with each bar 106 moving in an opposite direction from adjacent bars. However, it is to be understood that the number of reciprocating bars may be increased or decreased as desired and that numerous other configurations may be employed. Further, additional pneumatic cylinders may be used to independently move the reciprocating bars.

The pneumatic cylinders 108 move the reciprocating bars 106 back and forth relative to the semiconductor wafer, with the stroke of the bars 106 preferably being approximately equivalent to the diameter of the wafer plus two times the length of the reciprocating bars so that with each stroke the bar moves beyond the wafer surface. Alternatively, the reciprocating bars may oscillate so that the bar is continuously in contact with the wafer surface. The reciprocating bars 106 have greater rigidity than the endless belt of the previously described embodiments, providing a more stable system. The velocity of the reciprocating bars 106 is controlled by a control system 112 coupled to the pneumatic cylinders 108. The control system 112 is preferably configured to actuate the cylinders and drive the reciprocating bars 106 at a constant velocity. The constant velocity, linear motion of the polishing members 14c uniformly polishes the surface of the wafer. However, with some surface configurations it may be desirable to move the polishing members 14c in a non-uniform velocity profile. With the present embodiment, the control system may be configured to actuate the pneumatic cylinders 108 in accordance with a specific velocity profile to move the polishing members 14c at the required non-uniform velocity for uniform polishing. Although pneumatic cylinders 108 are employed in the present embodiment, other devices such as hydraulic cylinders, cams, stepping motors used with a ball screw etc., servomotors, linear motors, etc. may also be used to move the reciprocating bars 106.

The wafer 8 is preferably supported by the support assembly 16 shown in Figures 1-3 with the pivotal movement of the wafer within the wafer holder 34 positioning the wafer surface 9 parallel to the surface of the polishing members 14c. As described above in relation to Figures 1-3, the wafer holder 34 may rotate the wafer 8 relative to the polishing members 14c to uniformly planarize localized regions of the wafer surface. Alternatively, with some surface configurations uniform planarity may be obtained



without rotating the wafer. Although not shown, the support assembly 16 may be mounted for movement in a transverse direction relative to the reciprocating bars to move the wafer 8 transversely across the surface of the polishing members 14c.

5 The linear polisher 12d shown in Figure 9 includes a plurality of reciprocating bars 106d which are moved across a support plate 20d by a crank assembly 118. Polishing members 14d are mounted to the reciprocating bars 106d for polishing the surface of the wafer. The crank assembly 118 includes a plurality of crank arms 120 each coupled to a crank shaft 122 and one of the reciprocating bars. A motor (not shown) rotates the crank shaft 122, causing the crank arms 120 to move the reciprocating bars in a linear  
10 direction. As shown in Figure 9, the crank arms 120 move adjacent reciprocating arms in opposite directions. However, in other modifications two or more adjacent bars may be moved in the same direction. The linear polisher 12d is used with the support assembly 16 shown in Figures 1-3, which supports the wafer and positions the wafer surface parallel to the polishing members 14d.

15 In the embodiment of Figure 9, the velocity of the reciprocating bars 106d is not constant. Instead, the crank assembly 118 moves the reciprocating bars 106d at a sinusoidal velocity. Preferably, the semiconductor wafer is rotated at a variable velocity defined by the sinusoidal variations in the velocity of the polishing members 14d. With the crank assembly 118, the reciprocating bars 106d may be moved in a specific variable  
20 velocity profile to provide the desired polishing across the wafer surface.

Except as set forth above, the modifications of Figs. 4A-4B, 5A-5B, 6-7, 8 and 9 resemble those of the preceding modifications and the same reference numerals followed by the subscripts a-d, respectively, are used to designate corresponding parts.

25 It is to be understood that in the foregoing discussion and appended claims, the terms "wafer surface" and "surface of the wafer" include, but are not limited to, the surface of the wafer prior to processing and the surface of any layer formed on the wafer, including oxidized metals, oxides, spun-on glass, ceramics, etc.

30 While the invention has been described with reference to a few specific embodiments, the description is illustrative of the invention and is not to be construed as limiting the invention. Various modifications may occur to those skilled in the art without

departing from the true spirit and scope of the invention as defined by the appended claims.

Approved for Release by NSA on 09-11-2013 pursuant to E.O. 13526

WHAT IS CLAIMED IS:

1. A wafer polishing machine for the chemical mechanical planarization of a surface of a semiconductor wafer with an abrasive polishing agent comprising:  
a wafer support assembly having a wafer holder shaped to receive said wafer and support said wafer with said surface projecting from said wafer holder;  
a linear polishing assembly having a polishing member positioned to engage said surface of said wafer, said polishing member being movable in a linear direction relative to said wafer to continuously apply a uniform polishing force across said surface of said wafer during operation of said wafer polishing machine for uniformly polishing said surface of said wafer.
2. The wafer polishing machine of Claim 1 in which said polishing member comprises a belt and a polishing material mounted to said belt.
3. The wafer polishing machine of Claim 2 in which said belt is movable in a continuous path, and further comprising a conditioning station positioned in said path of said belt for conditioning said polishing material during operation of said wafer polishing machine.
4. The wafer polishing machine of Claim 1 in which said polishing member comprises a plurality of reciprocating bars having a polishing material mounted to said bars, said bars being movable in a linear direction relative to said wafer.
5. The wafer polishing machine of Claim 4 in which said polishing assembly includes at least one actuating device coupled to said bars for moving said bars in a linear direction relative to said wafer.
6. The wafer polishing machine of Claim 5 in which said polishing assembly includes a control system coupled to said actuating device, which control system is configured for moving said bars in accordance with a selected velocity profile.

7. The wafer polishing machine of Claim 1 in which said polishing member is movable at a constant, linear velocity relative to said wafer to continuously apply said uniform polishing force across said surface of said wafer.

8. The wafer polishing machine of Claim 1 in which said wafer holder is rotatable relative to said polishing member at a rate of at most approximately 1/10 of the velocity of said polishing member, said polishing member being moved in said linear direction relative to said wafer so that when said wafer holder is rotated, the angular velocity of said wafer relative to said polishing member is uniform across said surface of said wafer.

9. The wafer polishing machine of Claim 1 in which said polishing member is impregnated with said abrasive polishing agent.

10. The wafer polishing machine of Claim 1 in which one of said support assembly and said polishing assembly includes a pivotal alignment device positioned to pivotally support one of said wafer holder and said polishing member relative to the other of said wafer holder and said polishing member with said surface of said wafer and said polishing member retained in parallel alignment during operation of said wafer polishing machine.

11. The wafer polishing machine of Claim 10 in which said alignment device is coupled to said wafer holder, said alignment device supporting said wafer holder and orienting said wafer holder to position said surface of said wafer parallel to said polishing member during operation of said wafer polishing machine.

12. The wafer polishing machine of Claim 10 in which said alignment device supports said polishing member and orients said polishing member parallel to said surface of said wafer during operation of said wafer polishing machine.

13. The wafer polishing machine of Claim 10 in which said alignment device includes a journal supported in a bearing, said journal being pivotal relative to said bearing about a pivot point located on said surface of said wafer.

14. The wafer polishing machine of Claim 13 in which said journal is formed in the shape of a section of a sphere and in which said pivot point is located at the center of said sphere.

15. The wafer polishing machine of Claim 13 in which said journal and said bearing are separated by a lubricant.

16. A method for uniformly planarizing the surface of a semiconductor wafer using a polisher having a movable polishing member comprising the steps of:

supporting said wafer with said surface of said wafer engaging said polishing member; and

moving said polishing member in a linear direction relative to said wafer to apply a uniform polishing force across said surface of said wafer to uniformly planarize said surface of said wafer.

17. The method of Claim 16 in which said moving step includes moving said polishing member at a constant velocity within the range of 50 to 150 feet per minute.

18. The method of Claim 16 in which said moving step includes moving said polishing member in accordance with a velocity profile selected to apply a uniform polishing force across said surface of said wafer.

19. The method of Claim 16, and further comprising the step of rotating said wafer relative to said polishing member.

20. The method of Claim 19 in which said step of rotating said wafer includes rotating said wafer at a velocity of at most approximately 1/10 the velocity of said polishing member, with the angular velocity of said wafer relative to said polishing member being uniform across said surface of said wafer.

21. The method of Claim 16 in which said moving step includes moving said polishing member in a continuous path in which said polishing member passes across said surface of said wafer, and further comprising the step of conditioning said polishing member as said polishing member travels in said continuous path.
22. The method of claim 16 which further comprises the step of pivoting one of said wafer and said polishing member relative to the other of said wafer and said polishing member until said surface of said wafer and said polishing member are substantially parallel.
23. A wafer polishing machine for planarizing a surface of a semiconductor wafer comprising:
  - a wafer holder shaped to receive said wafer and support said wafer with said surface of said wafer projecting from said wafer holder;
  - a polishing assembly associated with said wafer holder having a polishing member movable in a continuous path in which said polishing member engages and moves across said surface of said wafer in a linear direction relative to said wafer; and
  - a conditioning station positioned in said path of said polishing member downstream from said support assembly for conditioning said polishing member during operation of said polishing machine.
24. The wafer polishing machine of Claim 23 in which said polishing member comprises an endless belt and a polishing pad mounted to said endless belt.
25. The wafer polishing machine of Claim 23 in which said conditioning station includes a scraping element positioned to engage said polishing member downstream of said wafer holder.
26. The wafer polishing machine of Claim 23 in which said conditioning station includes at least one bath for submersing said polishing member in one of an acidic solution, a rinsing solution and a polishing agent solution.

27. The wafer polishing machine of Claim 23 in which said conditioning station includes at least one acid bath, at least one rinse bath and at least one polishing agent bath positioned in said path of said polishing member.

28. The wafer polishing machine of Claim 23 in which said conditioning station includes at least one injection nozzle for spraying said polishing member with one of an acidic solution, a rinsing solution and a polishing agent solution.

29. The wafer polishing machine of Claim 23, and further comprising a pivotal alignment device associated with one of said wafer holder and said polishing assembly, said alignment device being positioned to pivotally support one of said wafer holder and said polishing member with said surface of said wafer and said polishing member retained in parallel alignment during operation of said wafer polishing machine.

30. A wafer support assembly for supporting a semiconductor wafer for polishing a surface of said wafer comprising:

a journal member having a planar support surface for supporting said wafer, said journal member being formed in the shape of a section of a sphere in which said planar support surface is separated from the center of said sphere by a distance approximately equal to said thickness of said wafer such that when said journal member supports said wafer, said center of said sphere is located on said wafer surface; and

a bearing mountable to a support platform having a recessed region shaped to receive said journal member, said journal being pivotal within said recessed region relative to said bearing about said center of said sphere.

31. The wafer polishing machine of Claim 30 in which said journal and said bearing are separated by a lubricant.

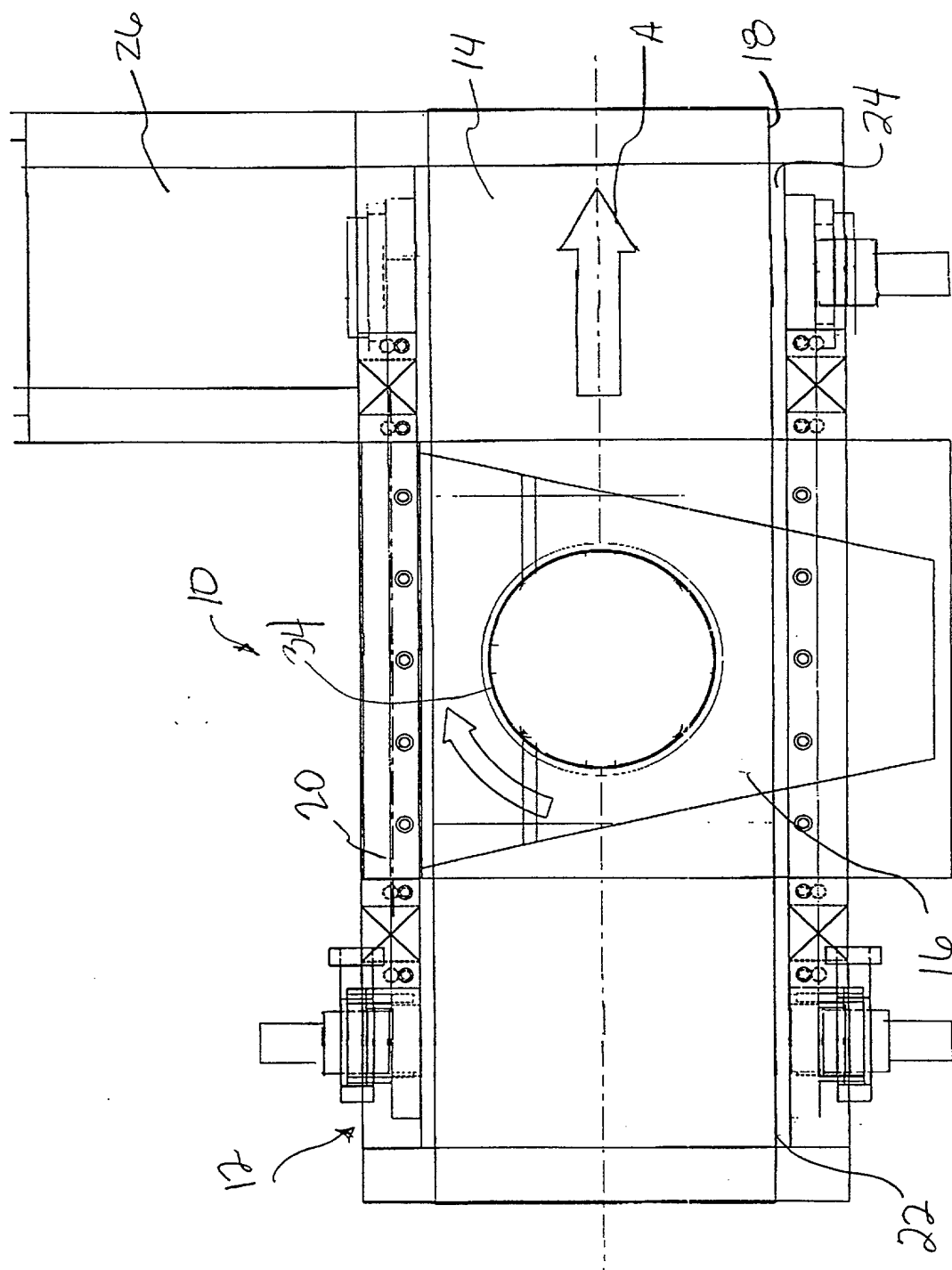
# ABSTRACT OF THE DISCLOSURE

A wafer polisher and method for the chemical mechanical planarization of semiconductor wafers. The polisher includes a wafer holder for supporting the semiconductor wafer and a linear polishing assembly having a polishing member positioned to engage the surface of the wafer. The polishing member is movable in a linear direction relative to the wafer surface to uniformly polish the surface of the wafer. A pivotal alignment device may be used to pivotally support one of the wafer holder and the polishing member relative to the other of the wafer holder and the polishing member with the surface of the wafer and the polishing member retained in parallel alignment during operation of the polisher. The polisher optionally includes a conditioning station for conditioning the polishing member.

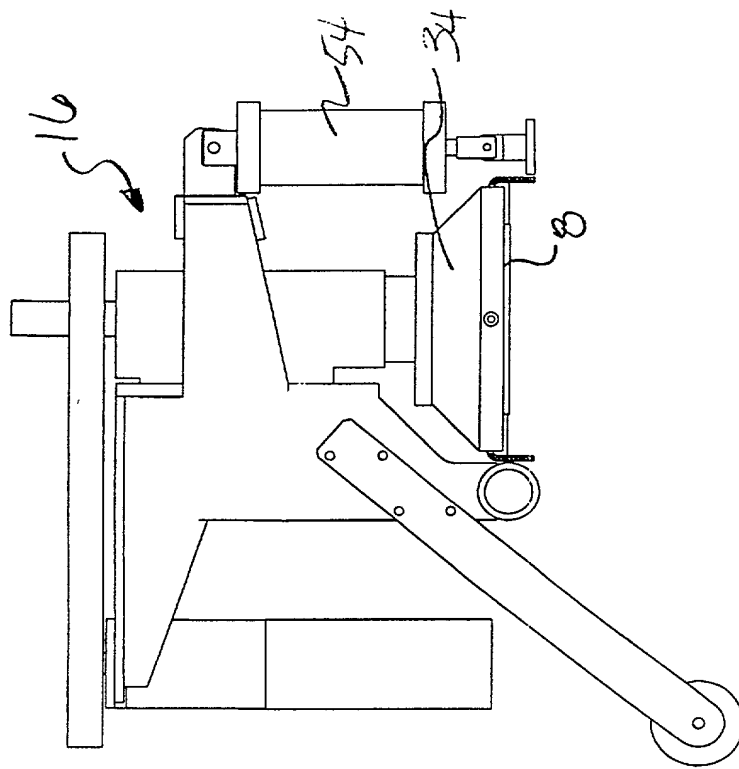
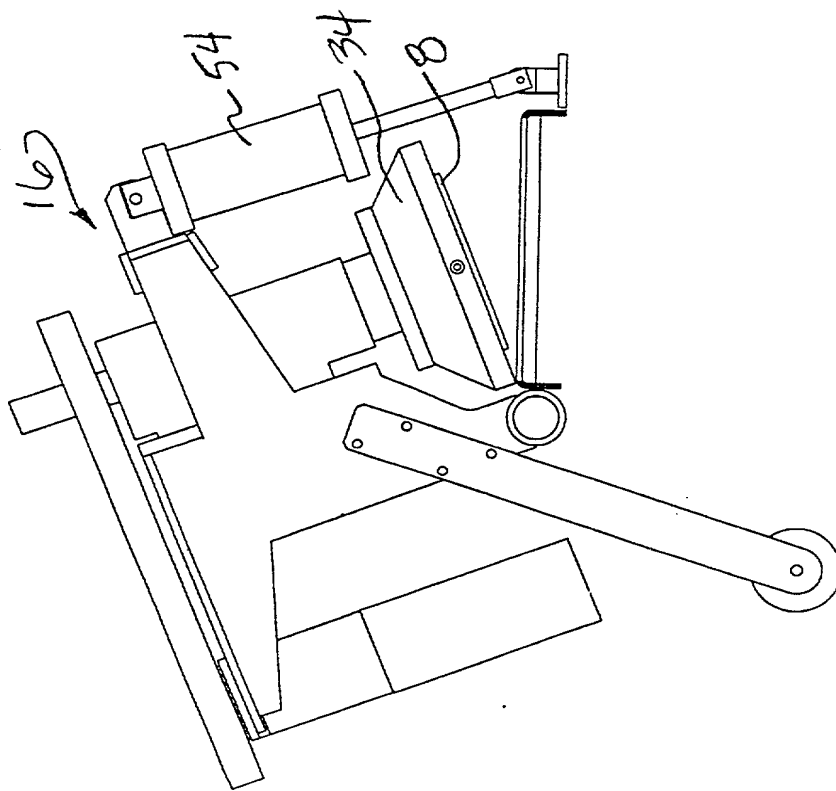




FIG. 2



**FIG. 3**



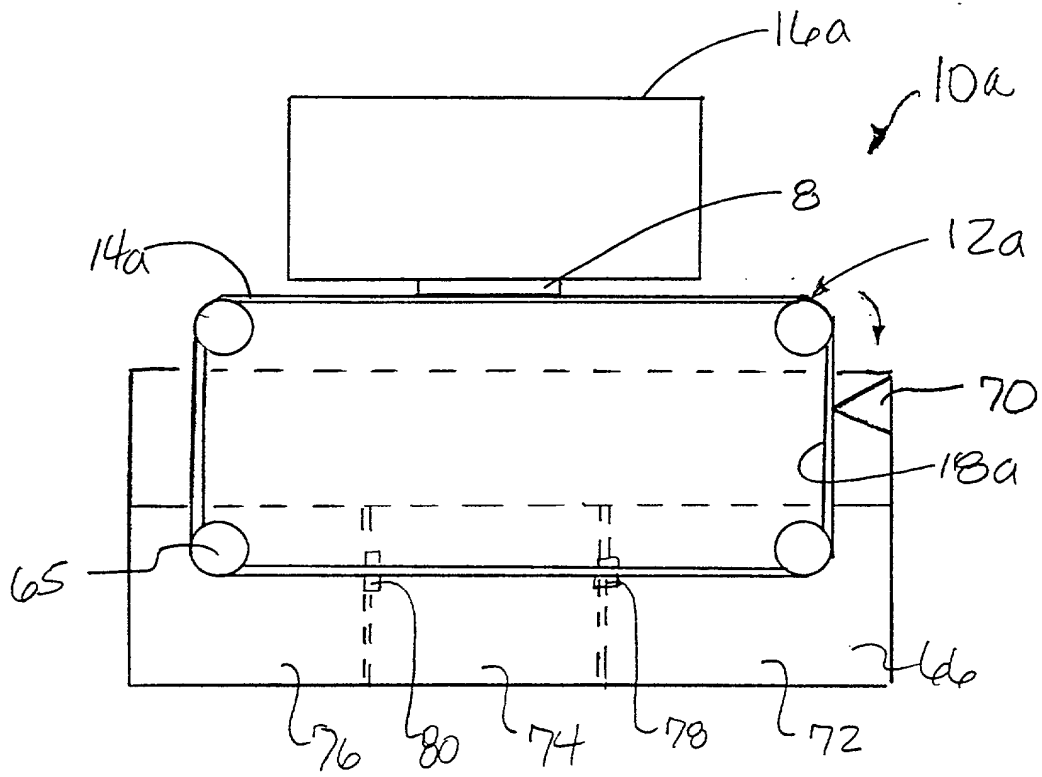


FIG. 5A

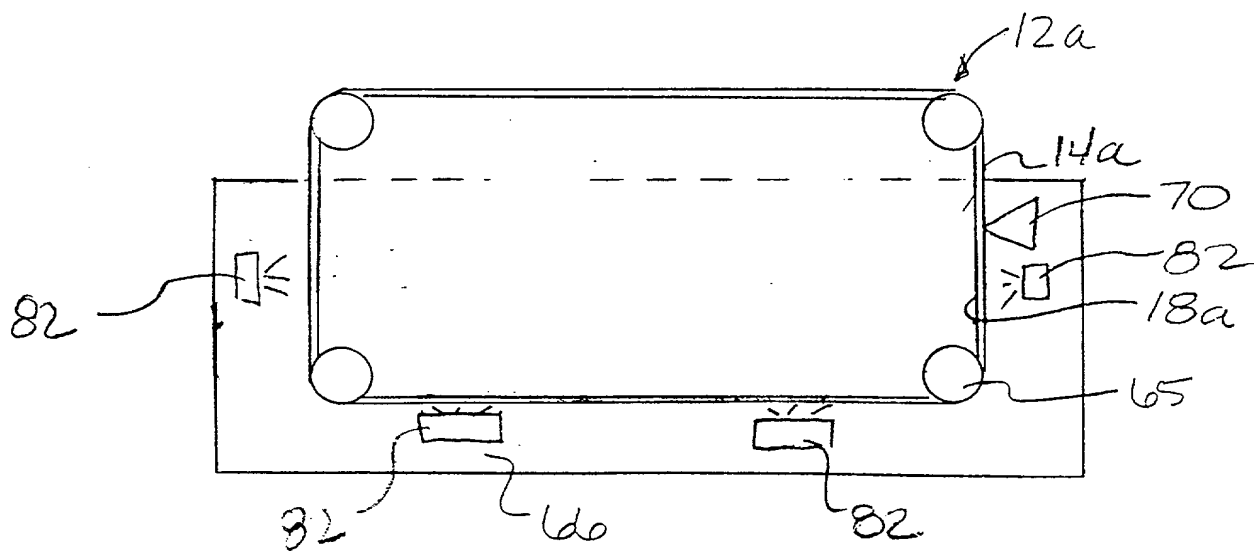


FIG. 5B

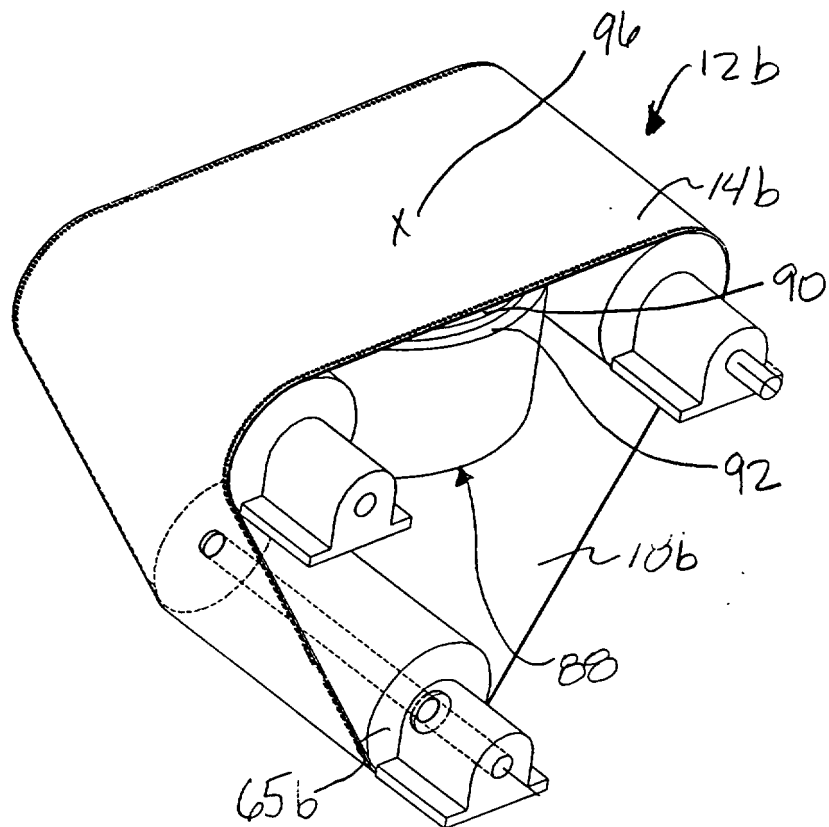


FIG. 6

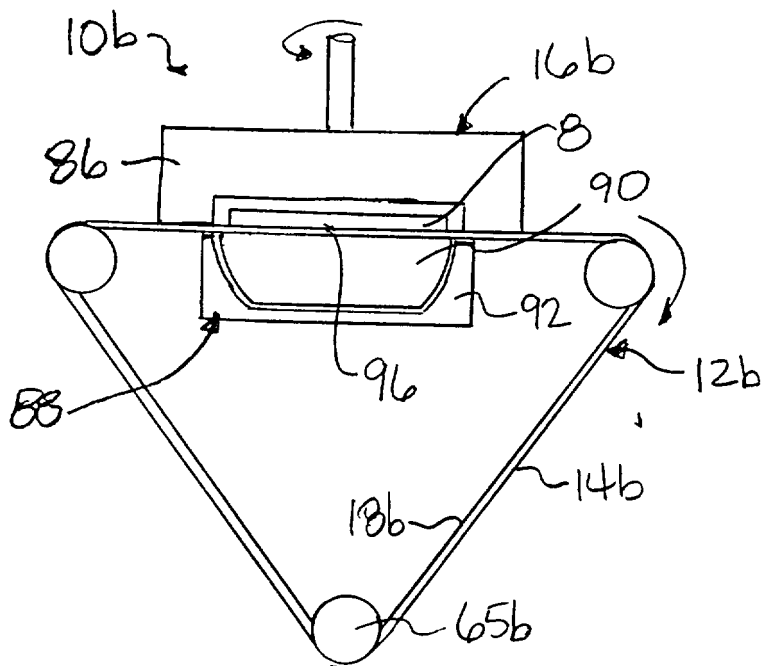
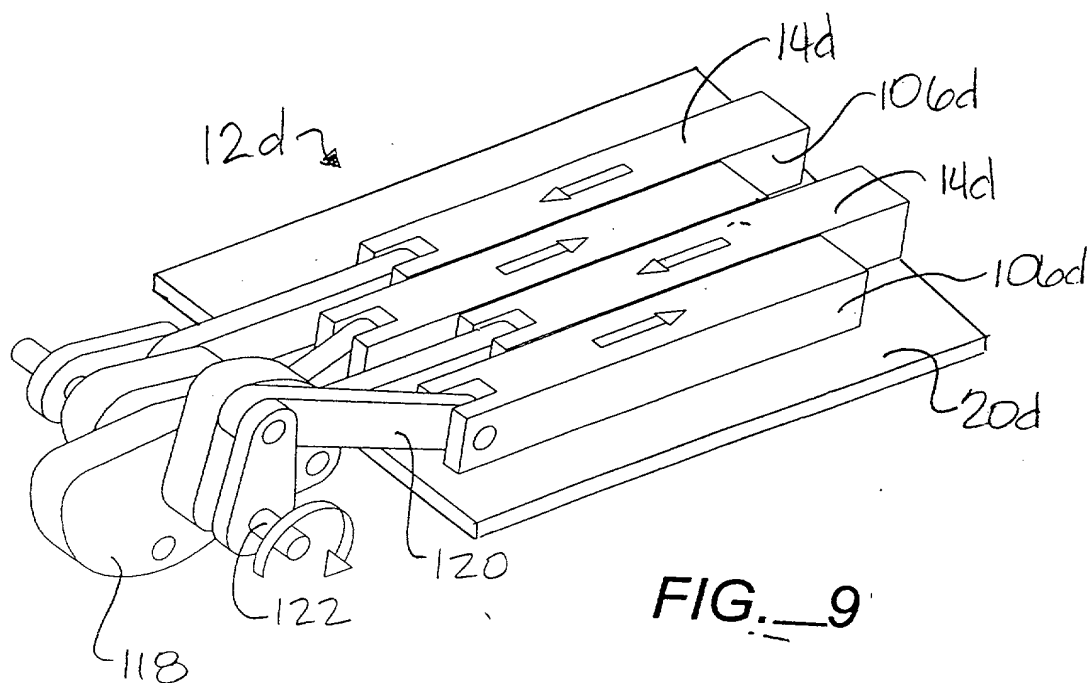
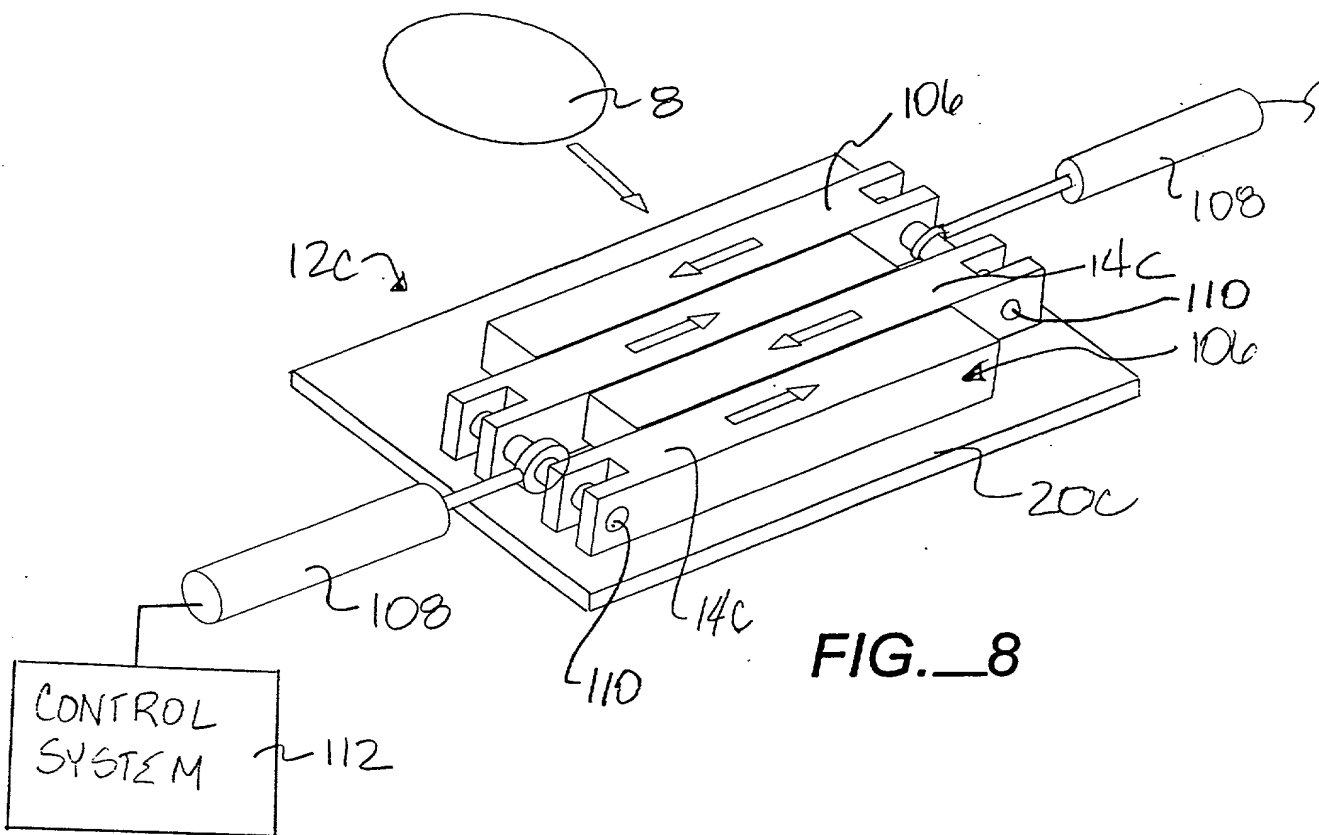
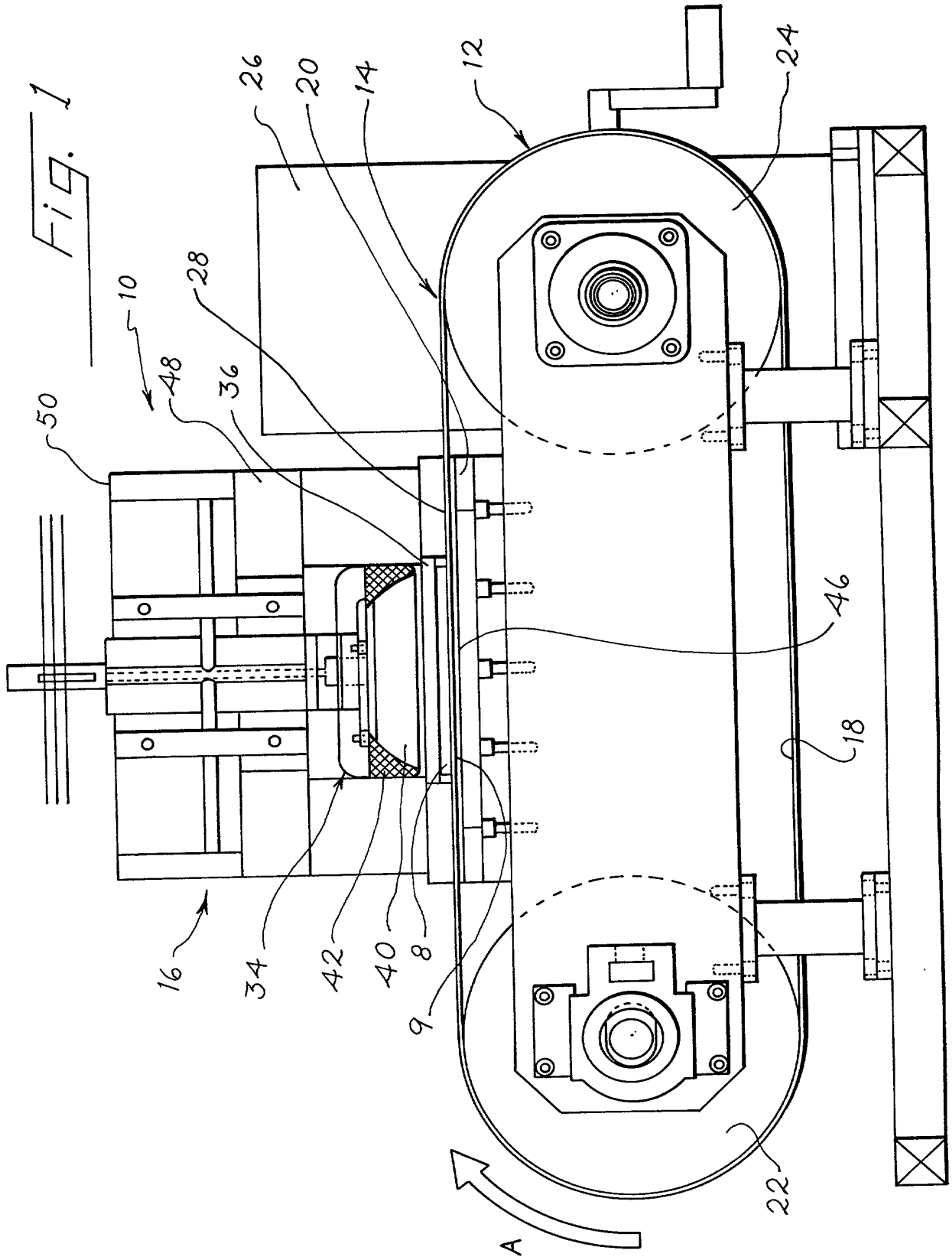


FIG. 7







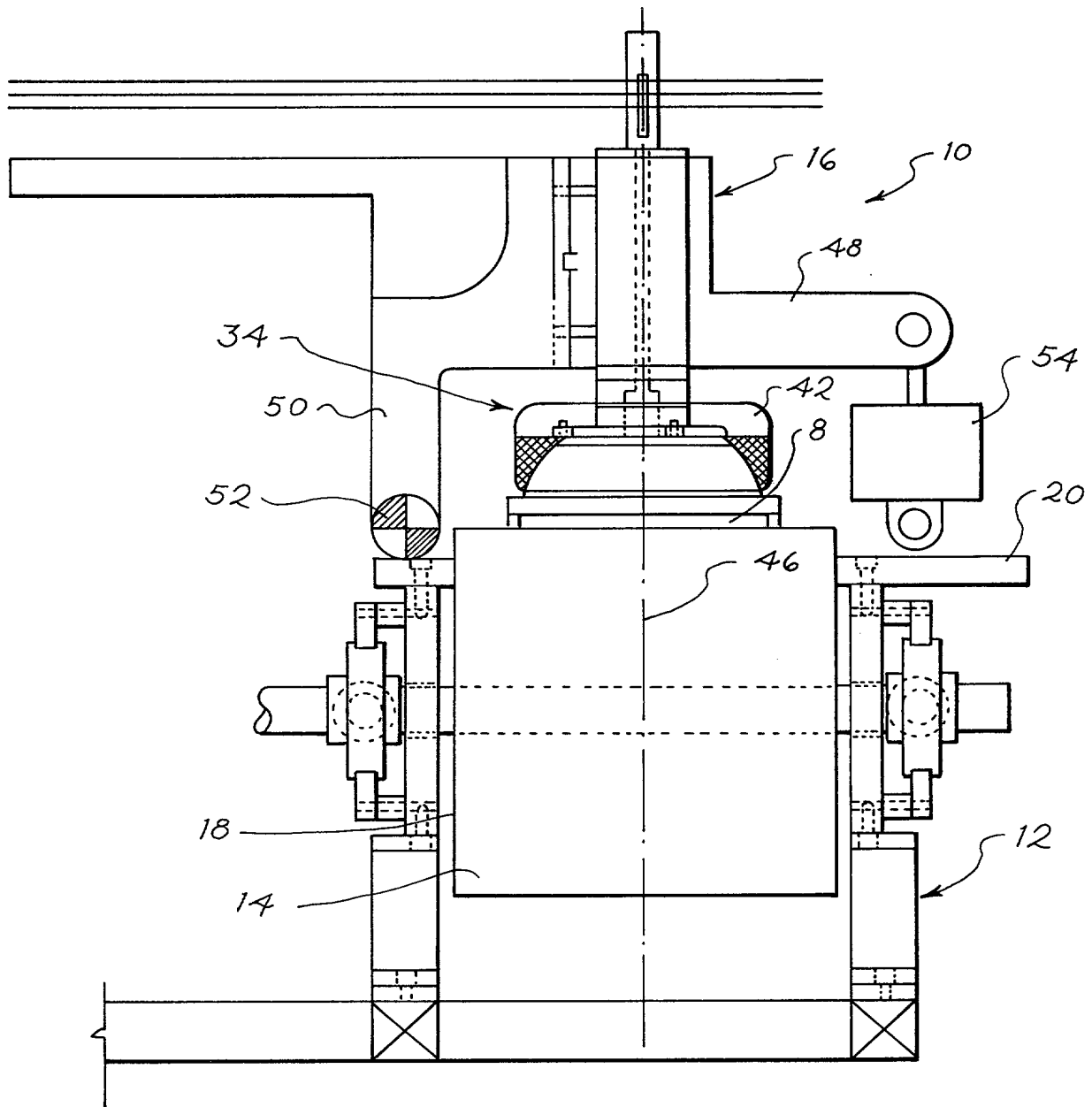
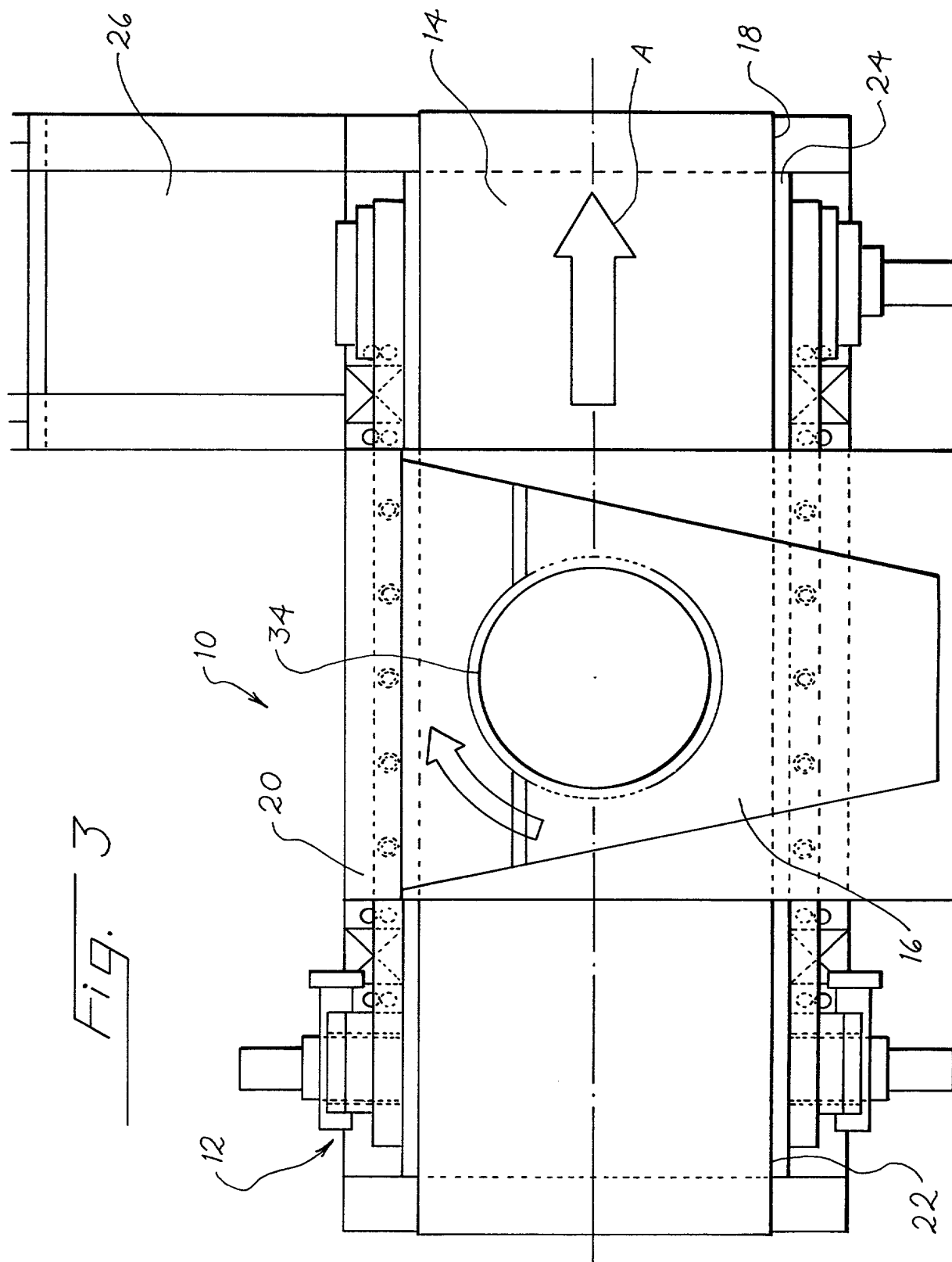


Fig. 2

Fig. 3





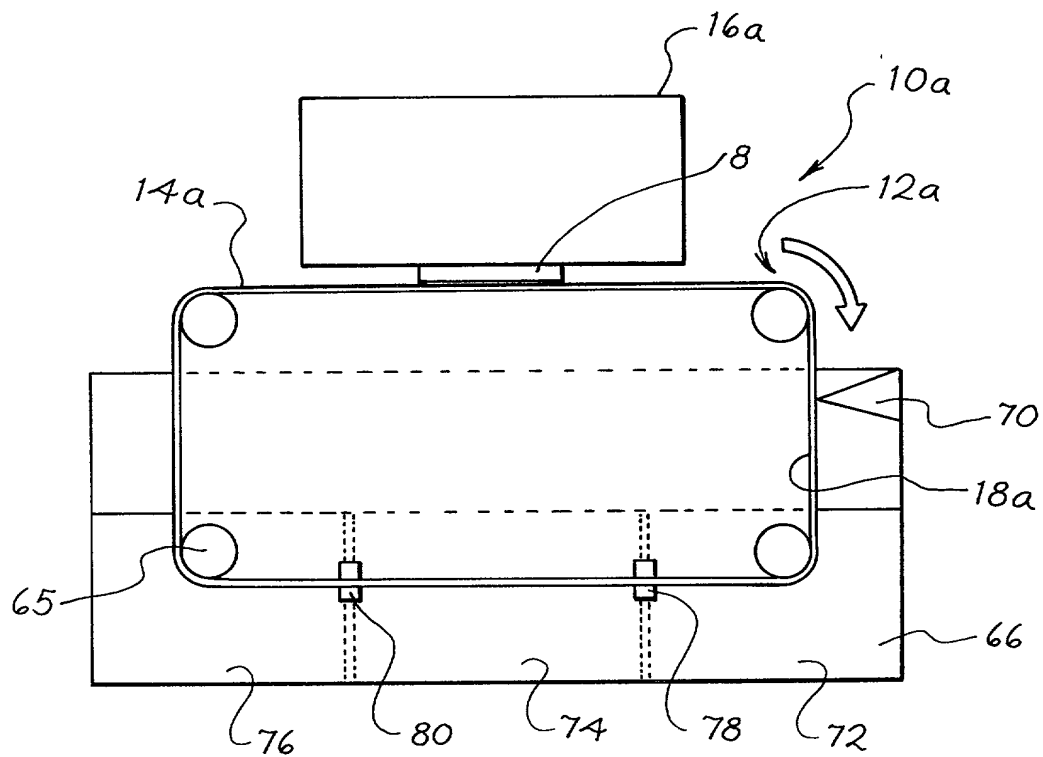


Fig. 5A

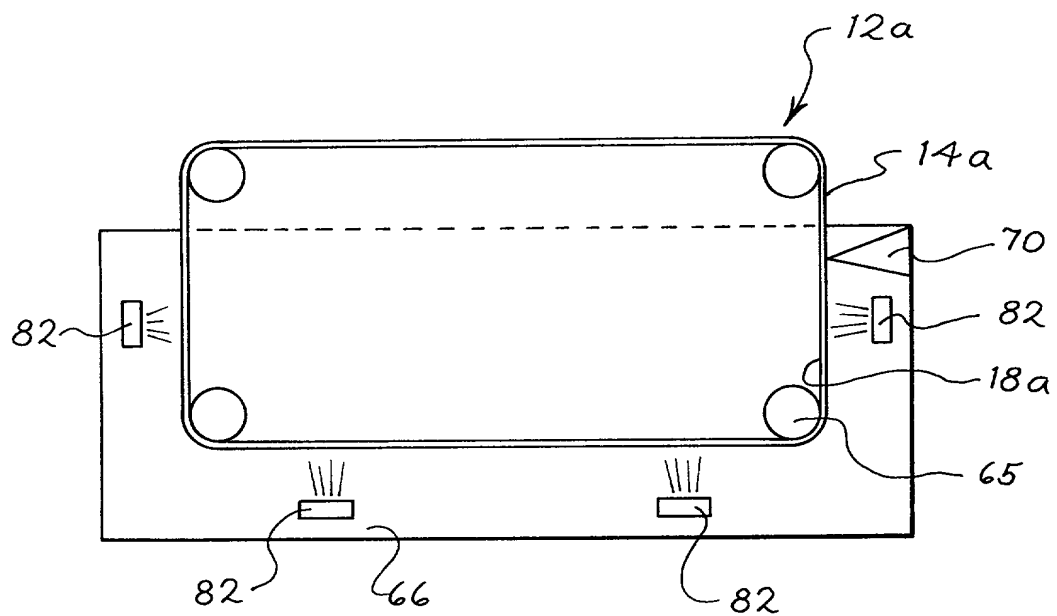
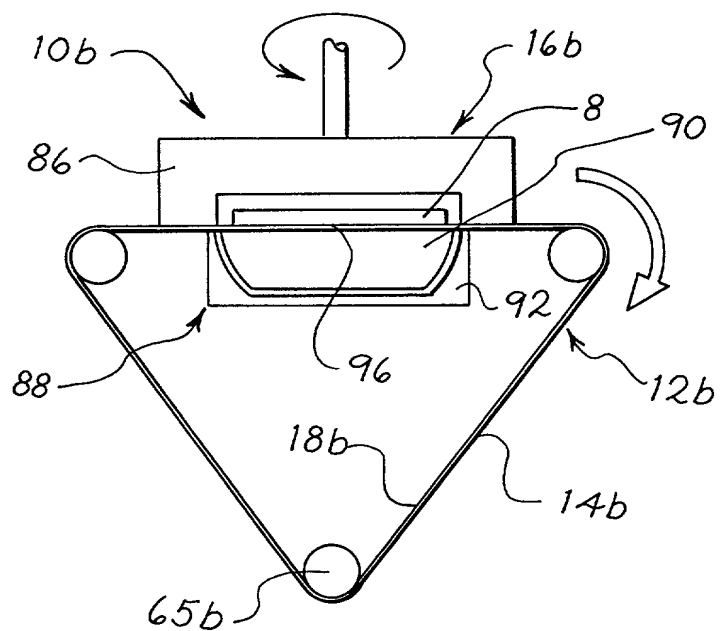
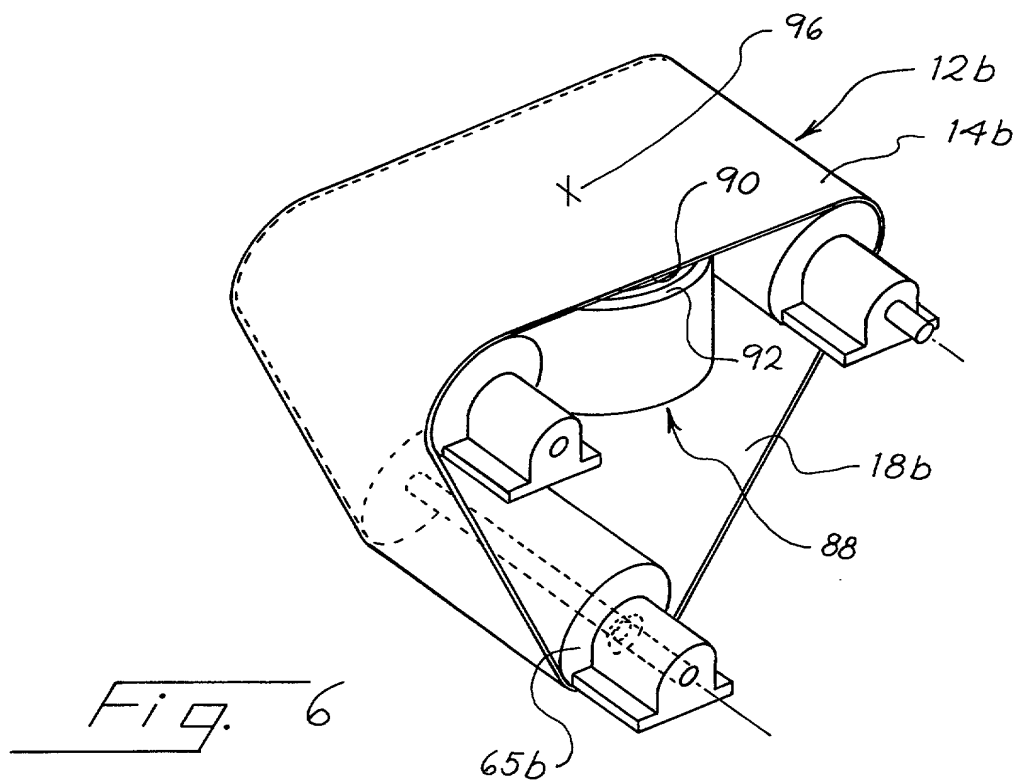
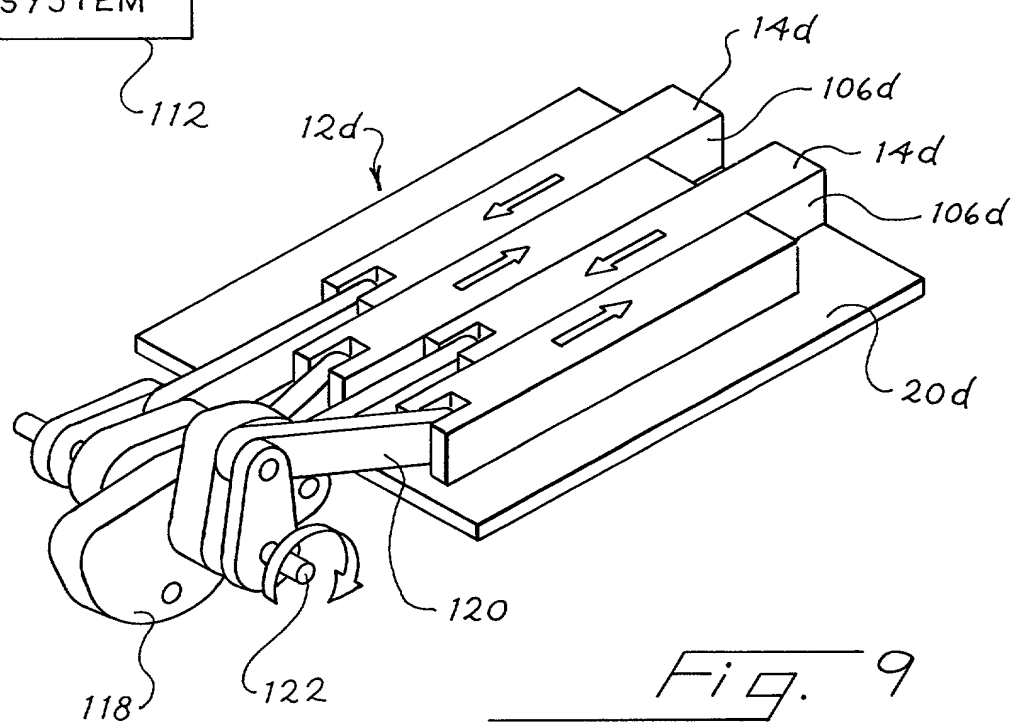
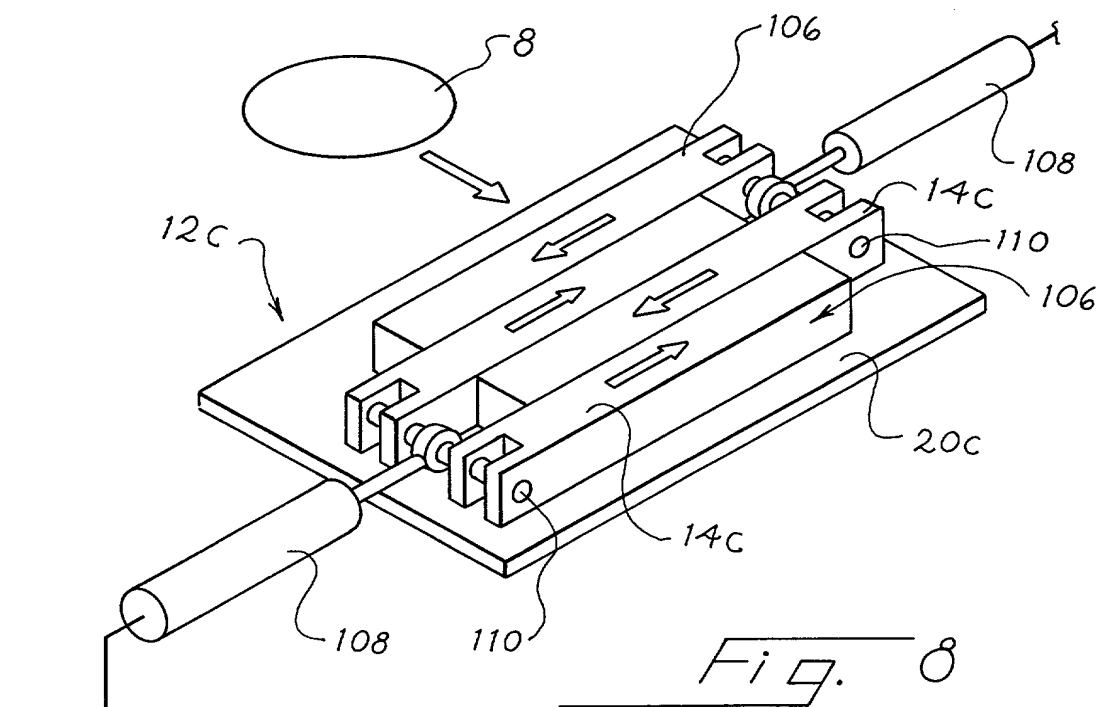


Fig. 5B





## DECLARATION FOR PATENT APPLICATION

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name. I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled *Linear Polisher and Method for Semiconductor Wafer Planarization*, the specification of which [x] is attached hereto [ ] was filed on [ ] as Application Serial No. [ ] and was amended on [ ].

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Patent Office all information known to me to be material to patentability as defined in 37 CFR §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed:

(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
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I hereby claim the benefit under Title 35, United States Code, §120 of any U.S. application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior U.S. application in the manner provided by the first paragraph of Title 35, U.S. Code, §112, I acknowledge the duty to disclose to the Patent Office all information known to me to be material to patentability as defined in 37 CFR §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status—patented, pending, abandoned)
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Direct all telephone calls to Janet E. Muller at (415) 494-8700. Address all correspondence to

Janet E. Muller  
FLEHR, HOHBACH, TEST, ALBRITTON & HERBERT  
Suite 3400, Four Embarcadero Center  
San Francisco, California 94111

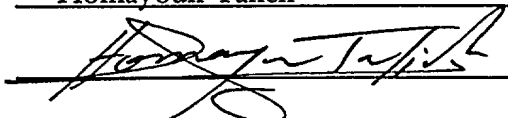
File No. A-59420/JEM

I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, §1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or  
first inventor:

Homayoun Talieh

Inventor's signature:



Date: 8/9/94

Residence:

Santa Clara County, California

Citizenship:

United States of America

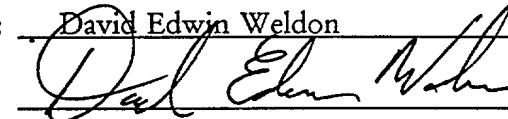
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joint inventor, if any:

David Edwin Weldon

Inventor's signature:



Date: 8/9/94

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Citizenship:

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23613 Skyview Terrace, Los Gatos, CA 95030

Full name of third  
joint inventor, if any:

Inventor's signature:

Date

Residence:

Citizenship:

Post Office Address:



Title: Linear Polisher and Method for Semiconductor Wafer Planarization

**REVOCATION OF POWER OF ATTORNEY  
AND  
APPOINTMENT OF NEW ATTORNEY**

The specification of the above-identified patent application:

☐ is attached hereto☒ was filed on August 9, 1994 as application Serial No. 08/287,658

I hereby revoke all previously granted powers of attorney in the above-identified patent application and appoint the following attorneys to prosecute said patent application and to transact all business in the Patent and Trademark Office connected therewith:

William A. Webb (28,277)

Michael P. Chu (37,112)

Please address all correspondence and telephone calls to William A. Webb in care of:

WILLIAM BRINKS HOFER GILSON & LIONE  
P.O. BOX 10395  
CHICAGO, ILLINOIS 60610  
(312) 321-4200

The undersigned hereby authorizes the U.S. attorneys named herein to accept and follow instructions from Dr. Wilbur L. Krusell as to any action to be taken in the Patent and Trademark Office regarding this application without direct communication between the U.S. attorney and the undersigned. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by the undersigned.

OnTrak Systems, Inc., a corporation  
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)  
certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application identified above, a copy of which is attached hereto.  
OR
- B. ☐ An assignment from the inventor(s) of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at Reel           , Frame           .  
OR
- C. ☐ A chain of title from the inventor(s), of the patent application identified above, to the current assignee as shown below:
1. From:                      To:                       
The document was recorded in the Patent and Trademark Office at  
Reel           , Frame           , or a copy thereof is attached.
  2. From:                      To:                       
The document was recorded in the Patent and Trademark Office at  
Reel           , Frame           , or a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

The undersigned has reviewed the assignment or all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Name: Dr. Wilbur C. KrusellTitle: CTO & Exec. VP of OnTrak Systems, Inc.Signature: Wilbur C. KrusellDate: March 14, 1996